



TN_122 FT232BL/BQ/BM Errata Technical Note

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The intention of this errata technical note is to give a detailed description of any known functional or electrical issues with the FTDI FT232BL/BQ/BM devices.
The current revision of the FT232BL is **revision A, released Jan 2005.**

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1 FT232B Revision

FT232B part numbers are listed in **Table 1**. The letter at the end of date code identifies the device revision.

Part Number	Package
FT232BM	32 Pin LQFP
FT232BL	32 Pin LQFP
FT232BQ	32 Pin QFN

Table 1 FT232B Part Numbers

This errata technical note covers the revisions of FT232B listed in **Table 2**.

Part Number	Revision	Notes
FT232BM	1	First device revision has no number after the part number
FT232BM	2	Second Device revision has a -1 after the part number
FT232BL FT232BQ	1	First device revision has no number after the part number

Table 2 FT232B Revisions

2 Errata History Table – Functional Problems

Functional Problem	Short description	Errata occurs in device revision
FT232BM/BQ/BL	Bitbang pulse width not stable	-, -1

2.1 Errata History Table – Electrical and Timming Specification Deviations.

Deviations	Short description	Errata occurs in device revision

3 Functional Problems of FT232BM/BL/BQ

3.1 First Revision

3.1.1 BitBang Mode variable Pulse Width

Introduction:

BitBang is a mode the device may be put into to allow free running data to be clocked in/out of the device without any control bits.

Problem:

The output may be clocked out at different speeds to allow for different pulse widths. However this clocking stage is not synchronized with the incoming data and can result in the pulse widths varying unexpectedly on the output.

Workaround:

Set the clock divisor to 1 (baud rate = 3,000,000) and pad the data field with extra 1's or 0's to achieve the required pulse width for each bit.

Package specific:

The effected packages are listed in Table 3

Package	Applicable (Yes/No)
FT232BM	Y
FT232BL	Y
FT232BQ	Y

Table 3

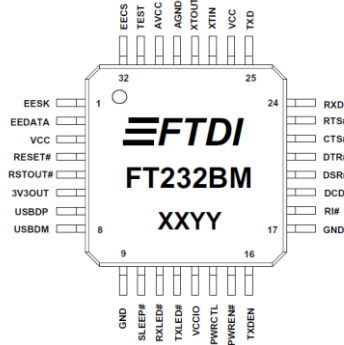
4 Electrical and Timing specification deviations of FT232BM/BQ/BL

4.1 First Revision

There are no known electrical or timing problems with any revision of silicon.

5 FT232B Package Markings

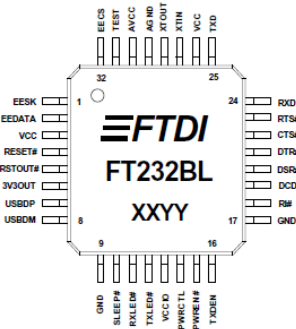
FT232BM is available in a non-RoHS Compliant package, 32 pin LQFP. An example of the markings on the package is shown in Figure 5-1.



Note that there are two date code formats used -
 XXYY = Date Code where XX = 2 digit year
 number, YY = 2 digit week number; or XYY-N
 where X = 1 digit year. Number, YY = 2 digit
 week number, and -N is an integer.

Figure 5-1 Package Markings – FT232BM

FT232B is available in two RoHS Compliant packages, 32 pin LQFP and 32 pin QFN. An example of the markings on each package is shown in Figure 5-2.



Note that there are two date code formats used -
 XXYY = Date Code where XX = 2 digit year
 number, YY = 2 digit week number; or XYY-N
 where X = 1 digit year. Number, YY = 2 digit
 week number, and -N is an integer.

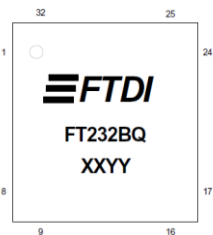


Figure 5-2 Package Markings – FT232BL/FT232BQ

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Appendix C – Revision History

Version Draft	First Draft	04/05/2010
Version 1.0	First Release	19/11/2010